



2016 Spintronics Workshop on LSI

June 13, 2016, 19:00-22:10

Hilton Hawaiian Village, Honolulu, Hawaii, USA



General Information

The 2016 Spintronics Workshop on LSI will be held at Hilton Hawaiian Village, Honolulu, Hawaii, on June 13, 2016, in conjunction with the VLSI Symposium on Technology as a Satellite Workshop of the 2016 VLSI Symposia. The workshop will focus on spintronics-based LSI technologies for high performance and ultra low power systems. Papers on current status, prospects and the remaining challenges in this field will be presented from invited speakers. The workshop is sponsored by Center for Innovative Integrated Electronic Systems (CIES), and Center for Spintronics Integrated Systems (CSIS), Tohoku University.

Registration

The workshop fee is free of charge, but registration is required.
http://www.cies.tohoku.ac.jp/2016_Spintronics_WS/registration
Deadline: June 10, 2016

Program

To be announced soon

Program Chair	Tetsuo Endoh (Tohoku Univ.)
Program Committee	H. S. Philip Wong (IEEE Executive Committee Chair of 2016 Symposia on VLSI Technology and Circuits) Tadahiro Kuroda (JSAP Executive Committee Chair of 2016 Symposia on VLSI Technology and Circuits) Hideo Ohno (Tohoku Univ.) Takahiro Hanyu (Tohoku Univ.) Masaaki Niwa (Tohoku Univ., JSAP Executive Committee Member of 2016 Symposia on VLSI Technology and Circuits)
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